深圳市智诚光电发展有限公司

Model Name:	" <u>N7 070NA-23A</u>						
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☐ Preliminary Specification ■ Final Specification							

For Customer's Acceptance	世界公司
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1. General Specifications

The specification N7 070NA-23A is a 7 "(1024x600) TFT Liquid Crystal Display module with LED Backlight unit, 40 pin LVDS interface, normally white transmissive display mode. This module will be applied to Consumer Electronics(CE Application)

No.	Item	Specification	Remark
1	LCD size	7.0 inch(Diagonal)	(E
2	Driver element	a-Si TFT active matrix	
3	Resolution	1024 × 3(RGB) × 600	
4	Display mode	Normally White, Transmissive	
5	Pixel size	0.1506(W) × 0.1432(H) mm	
6	Active area	154.2144(W) × 85.92(H) mm	
7	Module size	164.9(W) ×100(H) ×3.5(D) mm	Note 1
8	Surface treatment	Hard Coating	
9	Color arrangement	RGB-stripe	
10	Interface	LVDS	
11	View direction(Gray Inversion)	6 O'Clock	
12	Backlight power consumption	2.62 W (Typ.)	
13	Panel power consumption	0.4 W (Typ.)	
14	Weight	140g (Typ.)	
15	Source Driver IC		
16	Gate Driver IC		

2. Pin Assignment

FPC Connector is used for the module electronics interface. The recommended model is

FH12A-40S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	Р	Common Voltage	
2	VDD	Р	Power Voltage for digital circuit	
3	VDD	Р	Power Voltage for digital circuit	
4	NC		No connection	L PI
5	Reset	I	Global reset pin	
6	STBYB	-	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	采	- LVDS differential data input	
9	RXIN0+	IVI	+ LVDS differential data input	
10	GND	Р	Ground	
11	RXIN1-	I	- LVDS differential data input	
12	RXIN1+	I	+ LVDS differential data input	
13	GND	Р	Ground	
14	RXIN2-	I	- LVDS differential data input	
15	RXIN2+	I	+ LVDS differential data input	
16	GND	Р	Ground	
17	RXCLKIN-	I	- LVDS differential clock input	
18	RXCLKIN+	I	+ LVDS differential clock input	
19	GND	Р	Ground	
20	RXIN3-	I	- LVDS differential data input	
21	RXIN3+	I	+ LVDS differential data input	

22	GND	Р	Ground	
23	NC		No connection	
24	NC		No connection	
25	GND	Р	Ground	
26	NC	-	No connection	
27	DIMO	0	Backlight CABC controller signal output	
28	SELB	I	6bit/8bit mode select	Note1
29	AVDD	Р	Power for Analog Circuit	
30	GND	Р	Ground	
31	LED-	Р	LED Cathode	
32	LED-	Р	LED Cathode	
33	L/R	I	Horizontal inversion	Note3
34	U/D	I	Vertical inversion	Note3
35	VGL	Р	Gate OFF Voltage	
36	CABCEN1	I	CABC H/W enable	Note2
37	CABCEN0		CABC H/W enable	Note2
38	VGH	PX	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I: input, O: output, P: Power

Note1: If LVDS input data is 6 bits ,SELB must be set to High;

If LVDS input data is 8 bits ,SELB must be set to Low.

Note2: When CABC_EN="00", CABC OFF.

When CABC_EN="01", user interface image.

When CABC_EN="10", still picture.

When CABC EN="11", moving image.

When CABC off, don't connect DIMO, else connect it to backlight.

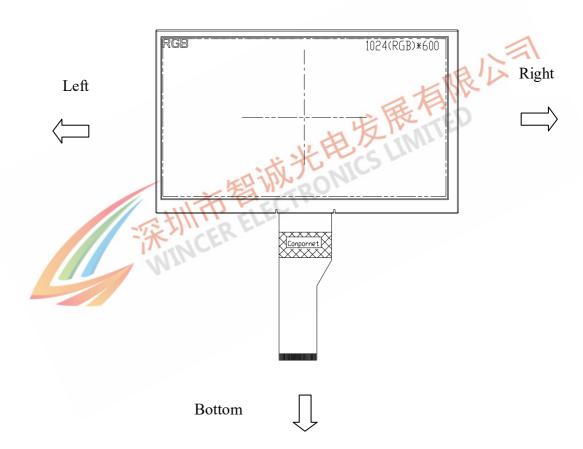
Note3: When L/R="0", set right to left scan direction.

When L/R="1", set left to right scan direction. When U/D="0", set top to bottom scan direction.

When U/D="1", set bottom to top scan direction.

Note: Definition of scanning direction. Refer to the figure as below:





3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

	(14010-1)				
Item	Symbol	Val	ues	Unit	Remark
item	Syllibol	Min.	Max.	Offic	Kelliaik
	DV_{DD}	-0.3	5.0	V	
	AV_DD	6.5	13.5	V	
Power voltage	V_{GH}	-0.3	42.0	V	
	V_{GL}	-20.0	0.3	V	3
	V _{GH} -V _{GL}	-	40.0	a.M.	الم
Operation Temperature	T _{OP}	-20	70月	$^{\circ}\mathbb{C}$	
Storage Temperature	T _{ST}	-30	80	$^{\circ}\!\mathbb{C}$	
LED Reverse Voltage	VR	ELIN, RON	5	V	Each LED
LED Forward Current	对州村	ELECT	60	mA	Each LED

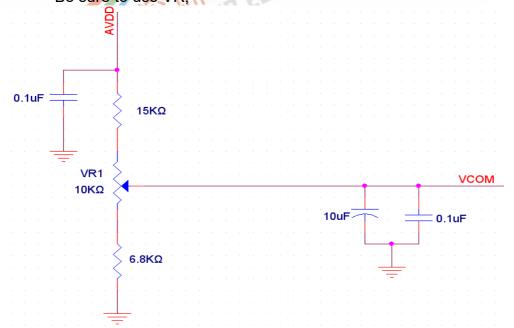
Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

3.1.1. Typical Operation Conditions

(Note 1)

ltom	Cumb al		Values	Unit	Remark	
Item	Symbol	Min.	Тур.	Max.	Onit	Nemark
	DV_{DD}	3.0	3.3	3.6	V	Note 2
Dower veltege	AV_{DD}	10.8	11	11.2	V	
Power voltage	V _{GH}	19.7	20	20.3	V	
	V_{GL}	-6.5	-6.8	-7.1	V	
Input signal voltage	V _{COM}	3.3	3.8	4.3	V	Note 4
Input logic high voltage	V _{IH}	0.7 DV _{DD}	-	DV_{DD}	V	Note 2
Input logic low voltage	V _{IL}	0	-	0.3 DV _{DD}	Z W	Note 3

- Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.
- Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.
- Note 3: LVDS, Reset.
- Note 4: Typ. V_{COM} is only a reference value, it must be optimized according to each LCM. Be sure to use VR;



3.1.2. Current Consumption

	Cymbol		Values		l loi4	Remark	
Item	Symbol	Min.	Тур.	Max.	Unit		
	I _{GH}	-	0.26	1	mA	V _{GH} =20V	
Current for Driver	I _{GL}	-	0.26	1	mA	V _{GL} = -6.8V	
Current for Driver	IDV_{DD}	-	35	60	mA	DV _{DD} =3.3V	
	IAV _{DD}	-	20	30	mA	AV _{DD} =11V	

3.1.3. Backlight Driving Conditions

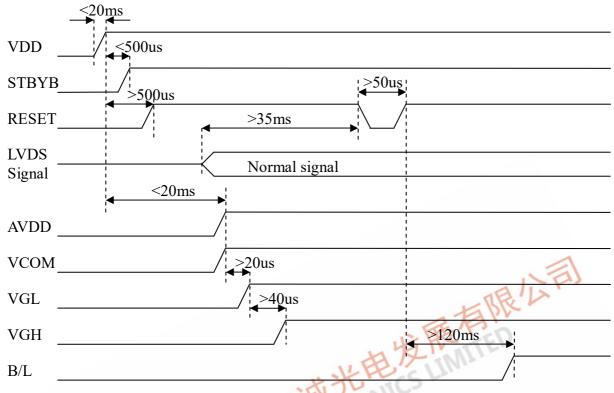
ltown	Cumbal		Values	l lm:t	Domonic	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Voltage for LED backlight	V_L	8.4	9.6	10.2	V	Note 1
Current for LED backlight	lι	出光	220	TIM.	mA	
LED life time	山书智	20,000	-	-	Hr	Note 2

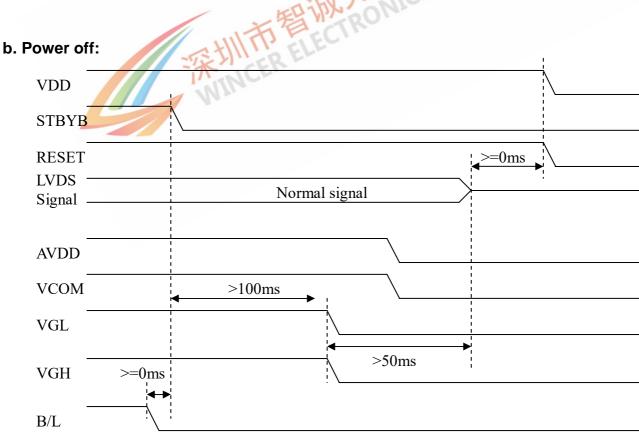
Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and I_L =265mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25° $\mathbb C$ and I_L =265mA. The LED lifetime could be decreased if operating I_L is lager than 265mA.

3.2. Power Sequence

a. Power on:



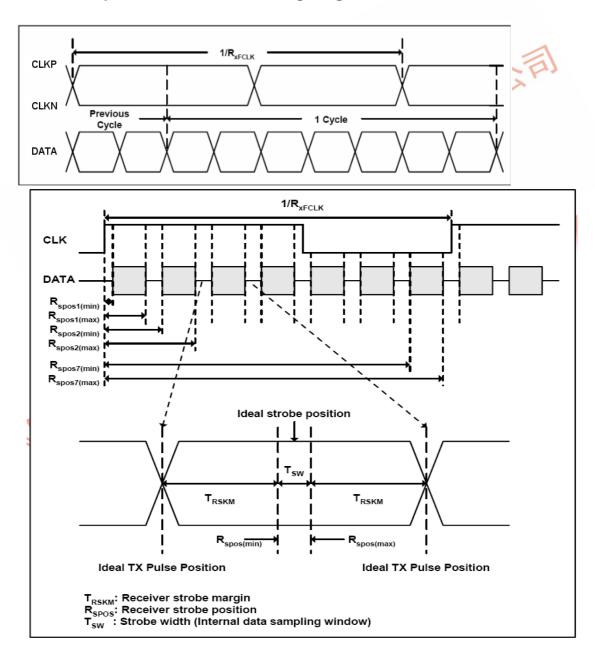


3.3. Timing Characteristics

3.3.1. AC Electrical Characteristics

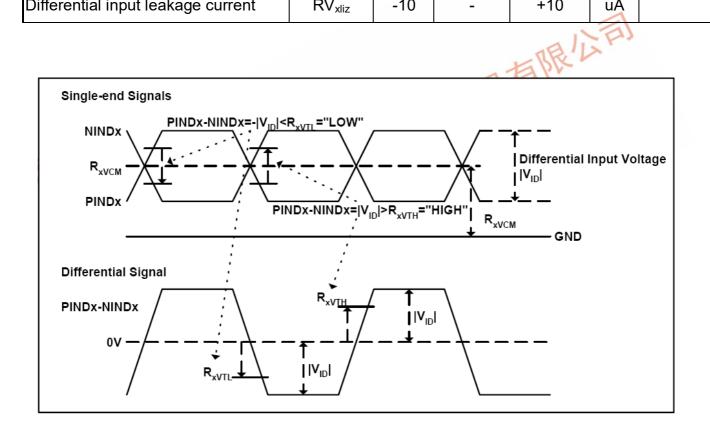
Parameter	Symbol		Values	Unit	Remark	
Parameter	Symbol	Min.	Тур.	Max.	Offit	Remark
Clock frequency	R _{xFCLK}	40.8	51.2	67.2	MHz	
Input data skew margin	T _{RSKM}	500	-	-	ps	
Clock high time	T _{LVCH}	-	4/(7* R _{xFCLK})	-	ns	
Clock low time	T _{LVCL}	-	3/(7* R _{xFCLK})	-	ns	

3.3.2. Input Clock and Data Timing Diagram



3.3.3. DC Electrical Characteristics

Parameter	Symbol		Values	Unit	Remark	
- aramotor		Min.	Тур.	Max.	O.IIIC	. comark
Differential input high Threshold voltage	R _{xVTH}	-	-	+0.1	V	R _{XVCM} =1.2V
Differential input low Threshold voltage	R _{xVTL}	-0.1	1	-	V	11XXVCM—1.2 V
Input voltage range (singled-end)	R _{xVIN}	0	ı	2.4	V	
Differential input common mode voltage	R _{xVCM}	V _{ID} /2	-	2.4- V _{ID} /2	V	
Differential voltage	V _{ID}	0.2	-	0.6	V	
Differential input leakage current	RV_{xliz}	-10	-	+10	uA	

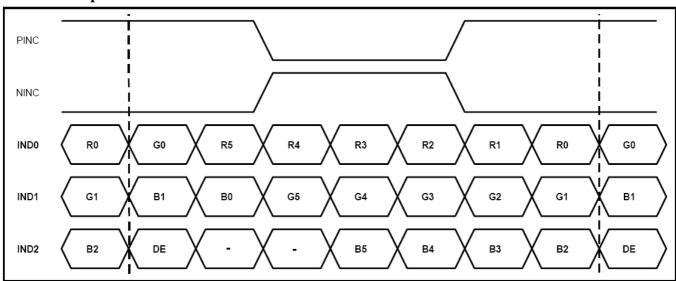


3.3.4. Timing

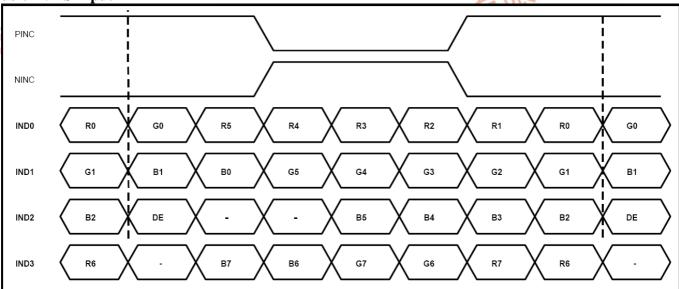
ltom	Symbol		Values	l lm:4	Domask	
Item		Min.	Тур.	Max.	Unit	Remark
Clock Frequency	fclk	40.8	51.2	67.2	MHz	Frame rate =60Hz
Horizontal display area	thd		1024		DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thb	90	320	376	DCLK	
Vertical display area	tvd		600		Н	
VS period time	tv	610	635	800	Н	
VS Blanking	thb	10	35	200	ARHIV	(A)

3.3.5. Data Input Format

6bit LVDS input



8bit LVDS input



Note: Support DE timing mode only, SYNC mode not supported.

4. Optical Specifications

ltem	Symbol Con	Condition	Values			Unit	Remark
item		Condition	Min.	Тур.	Max.	Offic	Remark
θL		Φ=180 (9 o'clock)	65	75	-		
Viewing angle (CR≥ 10)	θ_{R}	Ф=0°(3 o'clock)	65	75	-	dograa	Note 1
	θ_{T}	Φ=90°(12 o'clock)	60	70	-	degree	
	θ_{B}	Φ=270°(6 o'clock)	65	75	ı		
Posponso timo	Response time T_{OFF}		ı	10	20	msec	Note 3
Response une			-	15	30/	msec	Note 3
Contrast ratio	CR		500	800	SPO	-	Note 4
	W _X	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2
Color chromaticity Wy	加方智诚从	0.28	0.33	0.38	-	Note 5 Note 6	
Luminance	深	ICER ELES	400	450	-	cd/m	² Note 6
Luminance uniformity	Yu	IN-	70	75	-	%	Note 7

Test Conditions:

- 1. DV_{DD}=3.1V, I_L=265mA (Backlight current), the ambient temperature is 25 $^{\circ}$ C.
- 2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

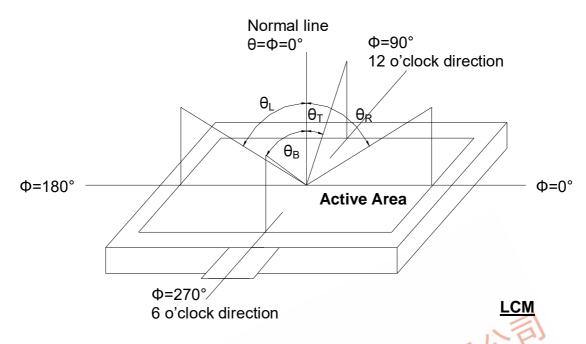


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1°/Heig ht: 500mm.)

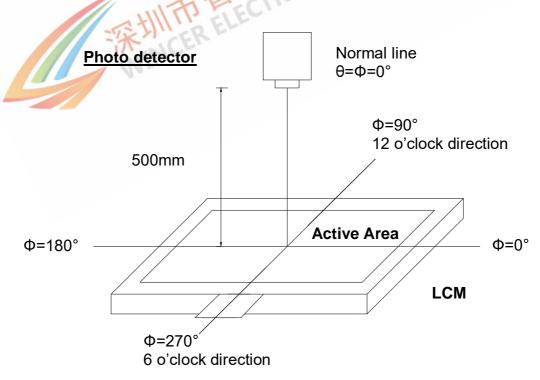


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

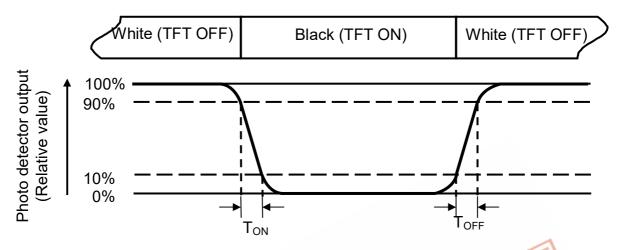


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD on the "White" state Luminance measured when LCD on the "Black" state Contrast ratio is measured at center point of LCD.

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of luminance:

Measured at the center area of the panel when LCD panel is driven at "white" state. The LED driving condition is I_L=265mA.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) =
$$\frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

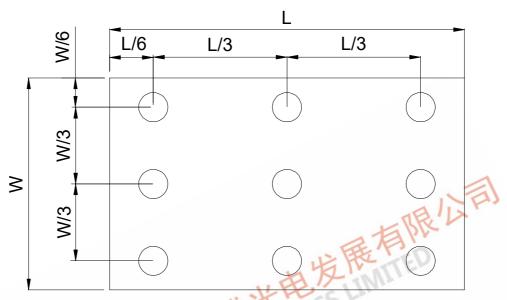


Fig. 4-4 Definition of measuring points

 \mathbf{B}_{max} : The measured maximum luminance of all measurement position. \mathbf{B}_{min} : The measured minimum luminance of all measurement position.

5. Reliability Test Items

Item	Test	Remark	
High Temperature Storage	Ta = 80°ℂ	240hrs	Note 1, Note3, Note 4, Note5
Low Temperature Storage	Ta = -30°C	240hrs	Note 1, Note3,
High Temperature Operation	Ts = 70°C	240hrs	Note 2, Note3, Note 4, Note5
Low Temperature Operation	Ta = -20°C	240hrs	Note 1, Note3,
Operate at High Temperature and Humidity	+60℃, 90%RH	240hrs	Note3, Note 4 Note5
Thermal Shock	[(-30°C 30min)→(8 100cycles	0°C 30min)]/cycle [,]	Note3, Note 4 Note5
Vibration Test	SweepSweep time:	~10Hz; Sweep Mode: Log 1Oct/min; Acceleration: rs for each direction of X,	制
Mechanical Shock	100G, 6ms, half sind direction of ±X, ±Y,	e wave, 3 times for each ±Z	
Package Vibration Test	Random Vibration : ISTA-3A 1Hz~200H Half hours for direct	z,Grms=0.53	
Package Drop Test		g), 60cm(weight>10kg);1 -edges and 1-corner	
Electro Static Discharge	Contact mode : 150 Air mode : 150pf , :	•	

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.
- Note 5: A certain level of Mura (non-uniformity) of dark / black image will happen several days after high temperature testing (H.T.T.). There is a slowly part recovery over a long time (several months). Such a long exposure time like in H.T.T. will normally not happen in a real application. Therefore the test H.T.T. was introduced to simulate cycles with normal conditions in-between but with the same total exposure time what show a significant reduced Mura.

The root cause is related to tension generated due to different amount of shrinking in the stack of layers in the polarizer sheet. The effect is more significant on larger displays like this size. An investigation into alternative polarizer material showed that there is no better alternative currently available.

6. General Precautions

6.1. Safety

- 1.Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.
- 2. The temperature for use is no more than 80 C, otherwise, only promise the function is OK, but the quality may be changed.

6.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
 - 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
 - 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

- 1. Store the module in a dark room where must keep at 25±10° and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
 - 3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

